

The listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. – 7. (Canceled)

8. (Currently Amended) An audio signal processing circuit comprising:
a thin film element formed over an insulating substrate;
a thin film resistor formed over the insulating substrate; and
a chip capacitor mounted over a flexible printed circuit substrate connected to the insulating substrate,

wherein one terminal of the thin film resistor is connected to the thin film element and the other terminal of the thin film resistor is connected to the chip capacitor.

9. (Previously Presented) The audio signal processing circuit according to claim 8, wherein the audio signal processing circuit comprises an input circuit and the input circuit comprises the thin film resistor and the chip capacitor.

10. (Previously Presented) The audio signal processing circuit according to claim 8, wherein the audio signal processing circuit comprises a feedback circuit and the feedback circuit comprises the thin film resistor and the chip capacitor.

11. (Previously Presented) The audio signal processing circuit according to claim 8, wherein the audio signal processing circuit comprises a smoothing circuit and the smoothing circuit comprises the thin film resistor and the chip capacitor.

12. (Original) The audio signal processing circuit according to claim 8, wherein P-type impurities are doped in the thin film resistor.

13. (Original) The audio signal processing circuit according to claim 8, wherein the thin film resistor has a resistance value of 80 k Ω or more.

14. (Original) An electronic equipment comprising the audio signal processing circuit according to claim 8, wherein the electronic equipment is one selected from the group consisting of a video camera, a digital camera, a head mounted display, a game machine, a car navigation system, a personal computer and a portable information terminal.

15. – 21. (Canceled)

22. (Currently Amended) A display device comprising:
a pixel portion formed over an insulating substrate;
a thin film element formed over the insulating substrate;
a thin film resistor formed over the insulating substrate; and
a chip capacitor mounted around the pixel portion and over the insulating substrate,

wherein one terminal of the thin film resistor is connected to the thin film element and the other terminal of the thin film resistor is connected to the chip capacitor.

23. (Previously Presented) The display device according to claim 22, wherein the display device comprises an input circuit and the input circuit comprises the thin film resistor and the chip capacitor.

24. (Previously Presented) The display device according to claim 22, wherein the display device comprises a feedback circuit and the feedback circuit comprises the thin film resistor and the chip capacitor.

25. (Previously Presented) The display device according to claim 22, wherein the display device comprises a smoothing circuit and the smoothing circuit comprises the thin film resistor and the chip capacitor.

26. (Original) The display device according to claim 22, wherein P-type impurities are doped in the thin film resistor.

27. (Original) The display device according to claim 22, wherein the thin film resistor has a resistance value of 80 k Ω or more.

28. (Original) An electronic equipment comprising the display device according to claim 22, wherein the electronic equipment is one selected from the group consisting of a video camera, a digital camera, a head mounted display, a game machine, a car navigation system, a personal computer and a portable information terminal.

29. (Currently Amended) A display device comprising:
a pixel portion formed over an insulating substrate;
a thin film element formed over the insulating substrate;
a thin film resistor formed over the insulating substrate; and
a chip capacitor mounted over a flexible printed circuit ~~substrate~~ connected to the insulating substrate,

wherein one terminal of the thin film resistor is connected to the thin film element and the other terminal of the thin film resistor is connected to the chip capacitor.

30. (Previously Presented) The display device according to claim 29, wherein the display device comprises an input circuit and the input circuit comprises the thin film resistor and the chip capacitor.

31. (Previously Presented) The display device according to claim 29, wherein the display device comprises a feedback circuit and the feedback circuit comprises the thin film resistor and the chip capacitor.

32. (Previously Presented) The display device according to claim 29, wherein the display device comprises a smoothing circuit and the smoothing circuit comprises the thin film resistor and the chip capacitor.

33. (Original) The display device according to claim 29, wherein P-type impurities are doped in the thin film resistor.

34. (Original) The display device according to claim 29, wherein the thin film resistor has a resistance value of 80 k Ω or more.

35. (Original) An electronic equipment comprising the display device according to claim 29, wherein the electronic equipment is one selected from the group consisting of a video camera, a digital camera, a head mounted display, a game machine, a car navigation system, a personal computer and a portable information terminal.

36. – 42. (Canceled)